

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Art Unit: Not assigned
Tomomi MOMOHARA	Examiner: Not assigned
Serial No: Not assigned (parent 09/511,620)	
Filed: April 21, 2004	
For: Semiconductor Integrated Circuit Device and Method of Testing the Same	

**PRELIMINARY AMENDMENT**

MAIL STOP PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Prior to the first Office Action in the present application, please enter and consider the following amendments and remarks:

**Amendments to the specification begin on page 2 of this paper.**

**Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.**

**Remarks/Arguments begin on page 4 of this paper.**